column format, restricted to a length of eight pages. The papers will be reviewed rigorously and scrutinized by expert reviewers. Depending on the reviewer's comments and ratings, the papers will be shortlisted for oral presentation. A Turnitin plagiarism report will be taken. Papers with plagiarism below 10% will only be considered for acceptance. The submitted papers should not to be published or under review in any other journal or conference. Notification of acceptance/ rejection of submitted papers will be sent to the authors by e-mail.

# **Advisory Committee**

Mar George Madathikandathil, Patron, VJCET Msgr. Dr. Pius Malekandathil, Manager, VJCET Rev. Fr. Paul Parathazham, Director, VJCET Dr. K K Rajan, Principal, VJCET Mr. Somy P. Mathew, Vice Principal, VJCET Dr. Amel Austine, HOD(CSE), VJCET Dr. Suji Pramila R, Prof., MBITS

## **Co-ordinators**

Ms. Neha Beegam P E, Asst. Prof., VJCET Mr. Andrews Jose, Asst. Prof., VJCET Dr. Neenu Daniel, Asso. Prof., VJCET

# Key Note Speaker

#### Dr Santosh Kumar M B

Professor and Head Division of Information Technology, School of Engineering, CUSAT



# For Further Details Contact:Ms. Neha Beegam P EPH : 7558897372

NB: The paper should be sent in the editable word format to **spincse@vjcet.org** 

# **Registration Fee**

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Students/Research Scholars	: <b>₹500</b>
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# **Publications**

All accepted papers will be published in proceedings of the conference with an ISBN number and modified version of the paper will be published in peer reviewed journal with an additional payment.

#### Publishing Partner: International Journal of Scientific Research in Science and Technology

Final Date for Paper Submission	: 24-2-2025
Intimation of Acceptance	: 01-3-2025
<b>Camera Ready Paper Submission</b>	: 03-3-2025
Date of Conference	: 20-3-2025
Venue	: R&D Semina
	Hall, VJCET



2<sup>nd</sup> INTERNATIONAL CONFERENCE ON SECURITY, PARALLEL PROCESSING, IMAGE PROCESSING & NETWORKING (SPIN 2K25) HYBRID MODE 20<sup>th</sup> March 2025 Time: 09.30 am - 4.00 pm Organized by Department of Computer Science & Engineering In association with

> CSI, R&D & A2Z Edulearning Hub







#### **About the College**

VJCET was established in the year 2001 by the Technical Educational Trust of the Catholic Diocese of Kothamangalam with the approval of AICTE and is affiliated to APJ Abdul Kalam Technological University. The college offers eight B.Tech (AD, CE, CSE, CSD, ECE, EEE, IT & ME), three M.Tech, MBA & BHMCT programs. The college has excellent infrastructure facilities with fully equipped laboratories, hostels, transportation facilities etc. The academic performance of VJCET has been consistently outstanding and produced a number of rank holders. The placement records of VJCET students have been excellent. Within 23 years after its inception, the college has grown as a front runner among the new generation Engineering Colleges of Kerala and is the preferred destination for technical education.

#### **Institute Vision**

Moulding Professionals par Excellence with Integrity, Fairness and Human Values

#### **Institute Mission**

- We commit to develop the institution into a Center of Excellence of International Standards.
- We guide and mould our students in the attainment of intellectual and professional competence for successfully coping with the rapid and challenging advancements in technologies and the ever changing world of business, industry and services.
- We help and support our students in their personal growth shaping them into mature and responsible individuals

- We strive to cultivate a sense of social and civic responsibility in our students, empowering them to serve the humanity.
- We promise to ensure a free environment where quest for the truth is encouraged

#### **Department of Computer Science & Engineering**

The department of Computer Science and Engineering offers B.Tech programme in Computer Science & Engineering with an intake of 180 students and M.Tech programme with an intake of 18 students. The department provides the students an environment that stimulates their intellectual growth and personality development. Infrastructure facilities are excellent with well-equipped computer labs, classrooms and libraries. A team of competent and dedicated faculty is the major asset of the department.

#### **Department Vision**

Moulding socially responsible and professionally competent Computer Engineers to adapt to the dynamic technological landscape.

#### **Department Mission**

- Foster the principles and practices of computer science to empower life-long learning and build careers in software and hardware development.
- Impart value education to elevate students to be successful, ethical and effective problem-solvers to serve the needs of the industry, government, society and the scientific community.
- Promote industry interaction to pursue new technologies in Computer Science and provide excellent infrastructure to engage faculty and students in scholarly research activities.

#### About SPIN 2K25

SPIN is a conference series organized by the Department of Computer Science & Engineering every year. It started in the year 2009 as a National Conference. International Conference on Security, Parallel Processing, Image Processing & Networking (SPIN 2K25) aims to provide an opportunity to all to have scientific discussions on the latest developments in the field of Computer Science and Engineering.

#### **Conference Topics**

The papers are invited on the following areas in general, but not limited to:

- Cryptography & Network Security
- Natural Language Processing (NLP)
- Architecture & Parallel Processing
- Image Processing
- Networking & Communication
- Cloud Computing & Big Data Processing
- Information Systems
- Data Mining & Warehousing
- Artificial Intelligence
- Machine Learning
- Data Science
- Internet of Things (IoT).

#### **Call for Papers**

Faculty, research scholars, scientists and graduate students from academic institutions, universities, R&D organizations and consultants and practicing engineers from industries/professional bodies are invited to participate and present their technical papers with original ideas.

#### **Information for Participants & Authors:**

• The papers to be presented should reach on or before February 24, 2025. Authors have to send one copy of the full paper in the IEEE double-